

	Search Text
1	(mosaic\$7)
2	(mosaic\$7) same circuit and revers\$5 near4 engineer\$4 and @ad<"20001018"
3	(mosaic\$7 photomosiac\$5) same circuit and revers\$5 near4 engineer\$4 and @ad<"20001018"
4	((circuit design layout) near2 extract\$5) with revers\$5 near3 engineer\$4
5	(mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4)
6	(mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4) and (concurrent\$4 shar\$4 ) near4 (enginneer user design\$4 layout)
7	(mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4) and (many plural\$4 concurrent\$4 shar\$4 ) near4 (enginneer user design\$4 layout)
8	(mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4) and (many plural\$4 concurrent\$4 shar\$4 ) near4 (enginneer user design\$4 layout) and @ad<"20001018"
9	(mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4) and (simultan\$6 ) near4 (enginneer user design\$4 layout) and @ad<"20001018"
10	(user owner\$5 engineer designer creator) with (id identi\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent simultan\$6)
11	(user owner\$5 engineer designer creator) near3 (regist\$5 id identi\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent simultan\$6) and (mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4)
12	(user owner\$5 engineer designer creator) near3 (regist\$6 id identi\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent\$4 realtime real adj time simultan\$6) and (mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4)
13	(user owner\$5 engineer designer creator) near3 (id identi\$6 regist\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent\$4 realtime real adj time simultan\$6) and (mosaic\$7 photomosiac\$5) same (circuit design layout extract\$5) same ( revers\$5 near3 engineer\$4)
14	(user owner\$5 engineer designer creator) with (id regist\$6 identi\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent\$3 simultan\$6) and (mosaic\$7 photomosiac\$5)
15	(user owner\$5 engineer designer creator) near3 (regist\$5 id identi\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent\$4 parallel simultan\$6) and (mosaic\$7 photomosiac\$5)
16	(user owner\$5 engineer designer creator) near3 (regist\$5 id identi\$6 access\$4 allow\$4 authori\$6 password account\$4) and (lock\$4) and (user owner\$5 enginner designer creator) with (merg\$4 concurrent\$4 parallel simultan\$6) and (mosaic\$7 photomosiac\$5) same ((circuit design layout) near3 extract\$5 or revers\$5 near3 engineer\$4)